

PATENT
81790.0214

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Art Unit: 2815
Tomomi MOMOHARA Examiner: J. Fenty
Serial No.: 09/511,620

Filed: February 23, 2000

For: SEMICONDUCTOR

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A PLURALITY OF WELLS, TEST METHOD OF TESTING THE SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, AND TEST DEVICE WHICH EXECUTES THE TEST METHOD
(As Amended)

being facsimile transmitted to the United States Patent and Trademark Office, Fax (703) 305-7722 on September 6, 2002.

Stephen R. Mason Reg. No. 41,112
Name September 6, 2002 Date
Signature

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(703) 305-7722 on September 6, 2002.

RESPONSE TO OFFICE ACTION

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**Assistant Commissioner for Patents
Washington, D.C. 20231
Attention: Box Amendment**

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action mailed June 6, 2002, the three-month statutory period during which to respond thereto expiring September 6, 2002, Applicant submits the following amendments and remarks.

AMENDMENT

Please amend the above-identified application as indicated below.

01 FC:103 162,00 CH

IN THE TITLE:

Please amend the title as indicated below:

-- SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A
PLURALITY OF WELLS, TEST METHOD OF TESTING THE SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE, AND TEST DEVICE WHICH EXECUTES THE
TEST METHOD --.